

Summary

Design Name	c3schem
Fitting Status	Successful
Software Version	O.40d
Device Used	XC2C64A-7-VQ44
Date	4-11-2011, 11:20PM

RESOURCES SUMMARY

Macrocells Used	Pterms Used	Registers Used	Pins Used	Function Block Inputs Used
24/64 (38%)	22/224 (10%)	22/64 (35%)	27/33 (82%)	37/160 (24%)

PIN RESOURCES

Signal Type	Required	Mapped	Pin Type	Used	Total
Input	0	0	I/O	19	26
Output	23	23	GCK/IO	3	3
Bidirectional	0	0	GTS/IO	4	4
GCK	3	3	GSR/IO	1	1
GTS	0	0	DGE/IO	0	-1
GSR	1	1			

GLOBAL RESOURCES

Signal mapped onto global clock net (GCK0)	START
Signal mapped onto global clock net (GCK1)	STOP
Signal mapped onto global clock net (GCK2)	CLK
Signal mapped onto global output enable net (GSR)	RESET

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